

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

KEN TAKEUCHI ET AL.

SERIAL NO.: UNASSIGNED

DIV. OF SERIAL NO.: 09/667,610

FILED: HERewith

FOR: NONVOLATILE SEMICONDUCTOR
MEMORY HAVING PLURAL DATA
STORAGE PORTIONS FOR A BIT LINE
CONNECTED TO MEMORY CELLS

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) GROUP ART UNIT: 2818
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) EXAMINER: H. HO
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) ATTY. DKT. NO. 001701.00140
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INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In accordance with Applicants' duty of disclosure, the enclosed information is submitted to the United States Patent and Trademark Office in connection with the above-identified application. The information is identified on the attached PTO Form-1449.


This application relies, under 35 U.S.C. §120, on the earlier filing date of prior U.S. Application Serial No. 09/667,610 filed on September 22, 2000. The references identified on the attached Form PTO 1449 were submitted to and/or cited by the Patent and Trademark Office in these prior applications and, therefore, copies are not required to be provided in this application. See 37 C.F.R. Section 1.98(d).

Applicant does not waive any right to take appropriate action to establish patentability over the listed documents should they be applied as references against the claims of the present application.

TAKEUCHI ET AL. (Divisional of U.S. Serial No. 09/667,610)

It is respectfully requested that the Examiner fully consider each of the documents, initial the enclosed Form PTO-1449 in the appropriate place to indicate that the document has been considered, and return a copy of the initialed form to the undersigned in accordance with MPEP Section 609.

Respectfully submitted,

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